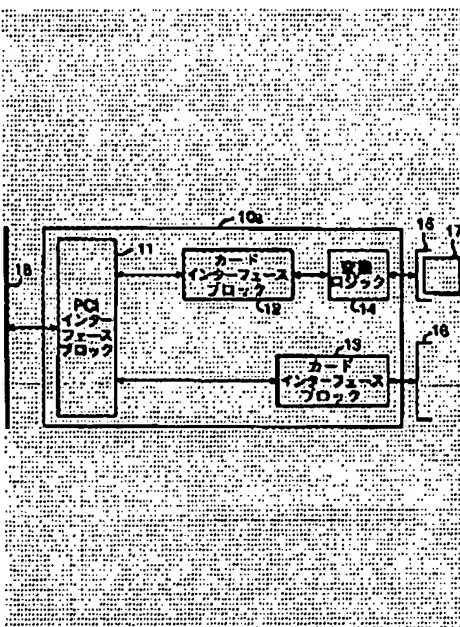


**MEMORY CARD BRIDGE****Publication number:** JP2002073522**Publication date:** 2002-03-12**Inventor:** KAMEDA KOJI**Applicant:** RICOH KK**Classification:****- International:** G06F13/12; G06F3/08; G06F13/12; G06F3/08; (IPC1-7); G06F13/12; G06F3/08**- European:****Application number:** JP20000255722 20000825**Priority number(s):** JP20000255722 20000825

Report a data error here

**Abstract of JP2002073522**

**PROBLEM TO BE SOLVED:** To provide a compact means capable of connecting each of information equipment and a small-sized memory card while sufficiently and effectively utilizing such merit that the small-sized memory card is small in size. **SOLUTION:** A PCI card bus bridge (PCIC) 10a for personal computer(PC) (information equipment) is provided with a PCI bus interface block 11, first and second two card interface blocks 12 and 13 based on the PCMCIA standard and a conversion logic 14 for converting the data of a small-sized memory card specification to a PC card specification or vice versa. Further, the PCIC 10a is provided with a small-sized memory card slot 15 and a PC card slot 16. Since the conversion logic 14 is provided inside the PCIC 10a, the merit of a small-sized memory card 17 can be sufficiently and effectively utilized.



Data supplied from the esp@cenet database - Worldwide